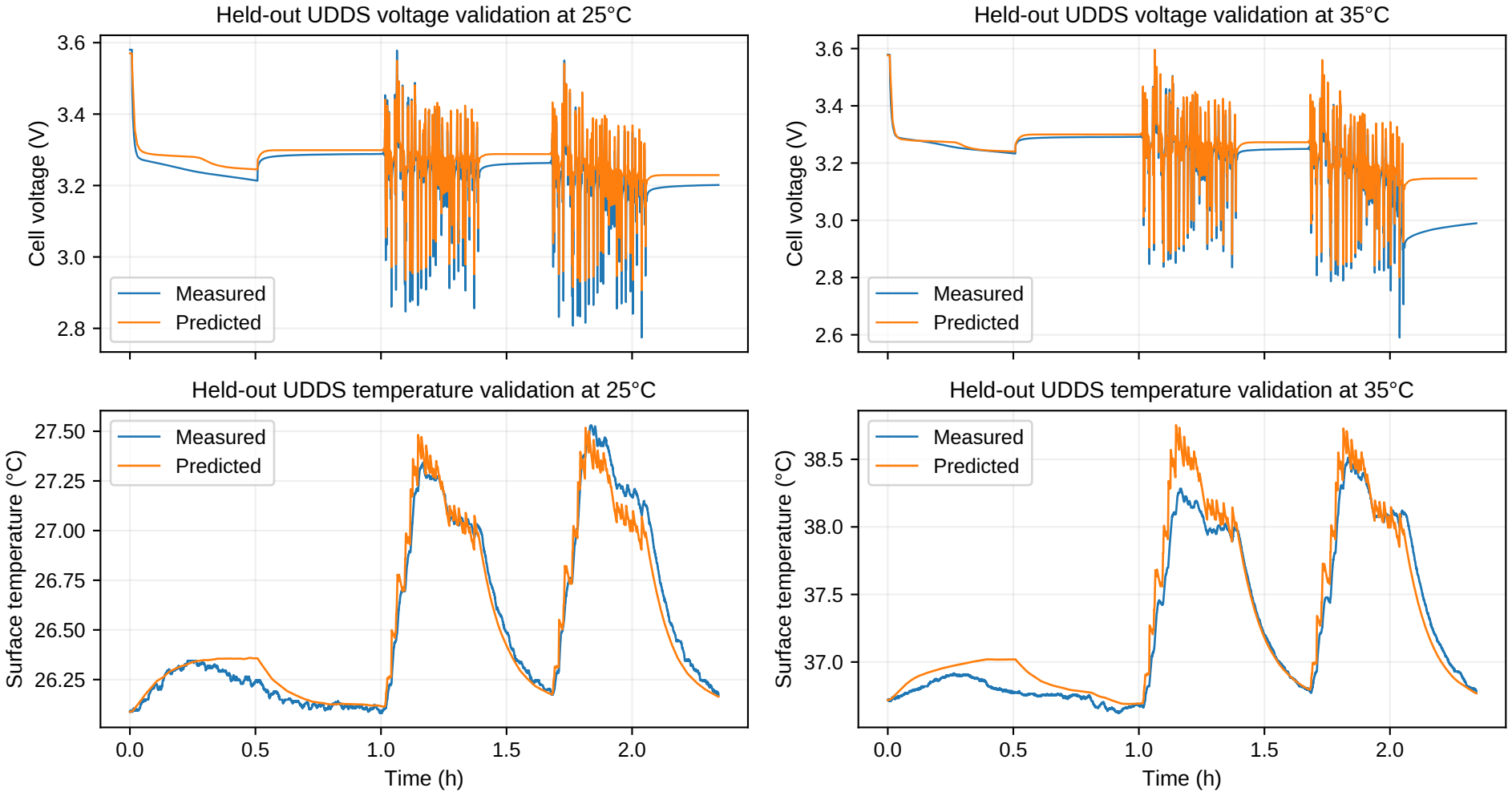

Held-out UDDS voltage validation at 25°C
Cell voltage (V)
Measured
Predicted
Held-out UDDS voltage validation at 35°C
Cell voltage (V)
Measured
Predicted
Held-out UDDS temperature validation at 25°C
Surface temperature (°C)
Measured
Predicted
Time (h)
Held-out UDDS temperature validation at 35°C
Surface temperature (°C)
Measured
Predicted
Time (h)